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026304 7590 11/14/2005

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January 4, 2006 (Date)

Customer Number: 026304
02/01/2006 WABDELRS 00000013 501290 10736934

01 FC:1501 1400.00 DA
02 FC:1504 300.00 DA
03 FC:8001 15.00 DA

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10736,934 | 12/16/2003 | Hisashi Yamauchi | NEC 15.938A | 5519 |

TITLE OF INVENTION: METHOD OF GENERATING TEST PATTERN FOR INTEGRATED CIRCUIT

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 02/14/2006 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|----------------|----------|----------------|
| LAMARRE, GUY J | 2133 | 714-727000 |

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Katten Muchin
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NEC Electronics Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Brian E. Hennings

Date

Registration No.

January 31 200651,271

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| | |
|------------------|-----------------|
| Date | Client / Matter |
| January 31, 2006 | 100806-00244 |
| From | Attorney Number |
| James J. Dade | 03290 |
| Phone | Fax |
| (212) 940-6754 | (212) 940-8986 |

Total number of pages, including cover letter: 3
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U.S. Patent Application No.: 10/736,934

METHOD OF GENERATING TEST PATTERN FOR ...

Our Reference No.: NEC 15.938A (100806-00244)

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